

Attorney Docket No. 11030.02

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Publication Number: US 2002/0167844 A1

Publication Date: November 14, 2002

Application No.: 10/039,518

Filed: November 8, 2001

Inventors: Han et al.

Title of Invention: METHOD AND APPARATUS FOR MULTIPLE BYTE OR PAGE  
MODE PROGRAMMING AND READING AND FOR ERASING OF A  
FLASH MEMORY ARRAYOffice of Publications  
ATTN: John Lachel  
Commissioner of Patents and Trademarks  
Washington, D.C. 20231REQUEST FOR CORRECTED PATENT APPLICATION PUBLICATION  
FOR PTO MISTAKE (37 CFR 1.221(a))

Applicant requests a corrected patent application publication since the patent application publication was printed with what may be a material mistake that is obvious from the USPTO records. Specifically, between the end of paragraph 0065 and the beginning of claim 1, the heading "CLAIMS" which appears in the application as filed is omitted from the patent application publication.

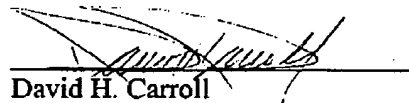
This Request is being filed within two (2) months of the publication date, therefore Applicant believes no fees are due in connection with this Request. In the event any fees are due in connection with this Request, please charge Deposit Account 04-1415.

Attorney Docket No. 11030.02

Any questions pertaining to this Request for Corrected Patent Application Publication  
should be addressed to the undersigned attorney.

Respectfully Submitted,

January 14, 2003

  
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## TRANSMISSION BY FAX

FROM: Altera Law Group, LLC  
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August 25, 2003  
Page(s): 5

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TO: Commissioner for Patents and  
Trademarks  
ATTN: **Mark Polutta**  
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FROM: David H. Carroll

Applicant: Park, et al.  
Filed: 1/21/2003  
Docket No.: 01770.0012-US-01

Serial No.: 10/349384  
Group Art Unit: 2818

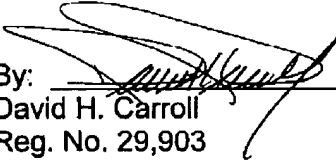
Title: NONVOLATILE MEMORY INTEGRATED CIRCUIT HAVING VOLATILE  
UTILITY AND BUFFER MEMORIES, AND METHOD OF OPERATION THEREOF  
Document Transmitted: Copy of "Request for Corrected Patent Application" as filed  
by facsimile in the U. S. Patent Office on January 14, 2003. (4 pages)

Per your request, attached is a copy of the Request for Corrected Publication filed on  
January 14, 2003

Respectfully submitted,

Altera Law Group, LLC  
Customer No. 22865

Date: August 25, 2003

By:   
David H. Carroll  
Reg. No. 29,903  
DHC/mar

CERTIFICATE UNDER 37 C.F.R. 1.6: I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office (Fax Number 703-746-3465) on August 25, 2003

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TO: **John Lachel** FAX #: **(703) 305-8568**  
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**COMMENTS:**

Patent Publication Number: **US 2002/0167844 A1**  
Publication Date: **November 14, 2002**  
Application No.: **10/039,518**  
Filed: **November 8, 2001**  
Inventors: **Han et al.**  
Title of Invention: **METHOD AND APPARATUS FOR MULTIPLE BYTE OR PAGE MODE  
PROGRAMMING AND READING AND FOR ERASING OF A FLASH MEMORY ARRAY**

**-- DEADLINE: January 14, 2003 --**

A Request for Corrected Patent Application Publication for PTO Mistake is transmitted herewith.

Please call with any questions you may have.

  
\_\_\_\_\_  
Originator's Signature

Original will be sent via: ☐ Mail ☐ Messenger ☐ Air Courier ☒ Will not be sent

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Reference# 459983-0008/11030.02

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Attorney Docket No. 11188.00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

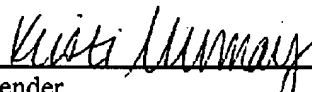

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FLASH MEMORY ARRAY

**CERTIFICATE OF FACSIMILE TRANSMISSION**

Office of Publications  
ATTN: John Lachel  
Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

The undersigned hereby certifies that the attached: REQUEST FOR CORRECTED PATENT APPLICATION PUBLICATION FOR PTO MISTAKE (37 CFR 1.221(a)) relating to the above application was transmitted by facsimile, facsimile number (703) 305-8568, addressed to the Office of Publications, ATTN: John Lachel, Commissioner of Patents & Trademarks, Washington, D.C. 20231 on January 14, 2003.

  
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